Unit-III / FkeâeF-III 11

6. Explain the function of ALE and IO/$\overline{M}$ signal pin of 8085. Discuss the register organization of microprocessor 8085.

ceeF›eâeØeememej 8085 ce ALE Deej IO/M emeiveue eheve keâ keÙee keâeÙe n? 8085 keâ jepemšj meie"ve keâe JÙeeKÙee keâeepeÙe~

7. Draw timing diagram of any one instruction of 8085 microprocessor. Give mnemonics of any five instructions of 8085 microprocessor and explain the operation performed by them.

ceeF›eâeØeememej 8085 keâ ekeâmee Skeâ eveoMe keâe meceÙe Deejke KeeeÛeÙe~ ekeâvne heeÛe eveoMee keâe eveceeevekeme Yee yeleeFÙe Deej mecePeeFÙe ekeâ Jen keâeve mee ØeÛeeueve keâjle n~

Unit-IV / FkeâeF-IV 12

8. Describe in brief important features of microprocessor 8086.

Write a program in assembly language to add all the even numbers from 1 to 100 using mnemonics of 8086.

8086 ceeF›eâeØeememej keâ mee#ehle ce ceKÙe ue#eCe yeleeFÙe~ Smecyeuee eueehe ce 8086 keâ eveceeevekeme keâjkeâ 1 me 100 lekeâ keâ mece vecyeje keâe pee[ve keâe Øee«eece eueeKeÙe~

9. Describe Eu and BIu of Microprocessor 8086. Discuss the various addressing modes in 8086.

8086 ceeF›eâeØeememej keâe Eu Deej BIu keâe mecePeeFÙe~ 8086 keâ eJeeYeVe helee eveieceve cee[e keâe Yee JÙeeKÙee keâeepeÙe~

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Roll No. _____

S-618

B.Sc.(Part-III) Examination, 2015

ELECTRONICS

Paper - III

(*Elements of Computer & Microprocessor*)

Time Allowed : Three Hours] [Maximum Marks : 75

Note : Answer five questions in all. Question No. 1 is compulsory. Attempt one question from each Unit.

keâue heeÛe ØeMvee keâ Gòej oeepeS~ ØeMve me. 1 DeeveJeeÙe n~ ØelÙekeâ FkeâeF me Skeâ ØeMve keâeepeS~

1. (a) Describe the construction of a memory cell. 3x10=20

mceele keâeeMekeâe keâe mejÛevee mecePeeFÙe~

(b) Explain the terms MAR, MAD with reference to memories.

MAR, MAD Šce keâe mceeleÙee keâ mevoYe ce mecePeeFÙe~

(c) What do you understand by term tristate switch?

e$eDeJemLeeÙe emJeÛe me Deehe keÙee mecePele n ?

(d) What is the difference between serial and parallel data transfer ?

ßeCeeyeæ Deej meceeveevlej [Še mLeeveevlejCe ce keÙee Devlej n ?

(e) Why data and address bus lines are multiplexed ?

[Še ueeFvee Deej helee ueeFvee keâe ceuŠehuekeme keÙee ekeâÙee peelee n?

(f) What do you understand by pipe lining ?

heeFhe ueeFeveie me Deehe keÙee mecePele n?

(g) What is the use of program counter ?

Øee«eece ieCekeâ keâe keÙee GheÙeeie nelee n ?

(h) Name the various flags of microprocessor 8085.

8085 ceeF›eâeØeememej keâe eJeeYeVe heleekeâeDee keâ veece yeleeFÙe ?

(i) Name various interrupt pins of microprocessor 8085 ?

8085 ceeF›eâeØeememej keâ eJeeYeVe DevleeÙeve ehevee keâ veece yeleeFÙe~

(j) What are mnemonics ?

eveceeevekeme keÙee nele n ?

Unit-I / FkeâeF-I 11

2. (a) Discuss the working of a simple diode ROM with the help of circuit.

(b) Explain What is the difference between Static and dynamic memory.

(De) heejheLe keâe meneÙelee me Skeâ mejue [eÙee[ROM keâe keâeÙe eJeeOe mecePeeFÙe~

(ye) emLej SJe ieelekeâ mceeleÙee ce keÙee Devlej nelee n?

3. How a memory is organised in two dimension matrix if the address range from OO 'Hex' to FF "Hex"?

What do you mean by memory addressing?

Skeâ mceele FkeâeF keâe eÉeJeceeÙe ceeŠkeme ce keâme meiee"le ekeâÙee pee mekeâlee n Ùeeo hele keâe meecee OO 'Hex' me 'FF' "Hesx" lekeâ ne~

mceele keâ heleeeYeieceve me Deehe keÙee mecePele n ?

Unit-II / FkeâeF-II 11

4. Describe the operation of a ring counter with the help of its logic circuit and wave forms what is a shift counter ?

JeueÙeieCekeâ keâe keâeÙeeJeeOe keâe lekeâ heejheLe Deej lejie ™he keâe meneÙelee me mecePeeFÙe~ eJemLeeheve ieCekeâ keÙee nelee n ?

5. Describe a Half Subtractor and full subtractor circuit with the help of block diagram and Truth Table.

Skeâ DeOe JÙeJekeâuekeâ Deej heCe JÙeJekeâuekeâ keâe yueekeâ DeejKe SJe melÙeceeheve meejCee okeâj JÙeeKÙee keâeepeÙe~