

Faculty of Engineering
University of Lucknow, Lucknow
Scheduled of B.Tech. Third Semester Practical Examination, 2018-19


Dated: 04.12.2018

COMPUTER SCIENCE & ENGINEERING (CS)

| SUBJECT | 29.12.2018 | | 30.12.2018 | | 31.12.2018 | | 01.01.2019 | |
|---|-----------------------|------------------------|-----------------------|------------------------|--|------------------------|-----------------------|------------------------|
| | I st Shift | II nd Shift | I st Shift | II nd Shift | I st Shift | II nd Shift | I st Shift | II nd Shift |
| Network Analysis & Synthesis Lab | CSP1 | CSP2 | | | | | | |
| Data Structure Lab | | | | | CSP1 | CSP2 | | |
| Numerical Tech. Lab | | | | | | | CSP1 | CSP2 |
| Digital Circuit & Logic Design Lab | | | CSP1 | CSP2 | | | | |
| ELECTRICAL ENGINEERING (EE) | | | | | | | | |
| Network Analysis & Synthesis Lab | | | | | EEP1 | EEP2 | | |
| EMMI Lab | EEP1 | EEP2 | | | | | EEP1 | EEP2 |
| Electrical Machine Lab - I | | | | | | | | |
| Analog Integrated circuits Lab | | | EEP1 | EEP2 | | | | |
| ELECTRONICS & COMMUNICATION ENGINEERING (EC) | | | | | | | | |
| Data Structure Lab | | | ECP1 | ECP2 | | | | |
| Digital Circuit & Logic Design Lab | ECP1 | ECP2 | | | | | | |
| Solid State Devices Lab | | | | | ECP1 | ECP2 | | |
| Signal & Systems Lab | | | | | | | ECP1 | ECP2 |
| CIVIL ENGINEERING (CE) | | | | | | | | |
| Fluid Mech. Lab | CEP1 | CEP2 | | | | | | |
| Strength of Materials Lab | | | CEP1 | CEP2 | | | | |
| Building Material & construction lab | | | | | CEP1 | CEP2 | | |
| Surveying Lab | | | | | | | CEP1 | CEP2 |
| MECHANICAL ENGINEERING (ME) | | | | | | | | |
| Fluid Mechanics Lab | | | MEP1 | MEP2 | | | | |
| Strength of Materials Lab | MEP1 | MEP2 | | | | | | |
| Material Science Lab | | | | | MEP1 | MEP2 | | |
| Machine Drawing Lab | | | | | | | MEP1 | MEP2 |
| Ist Shift (10.00 AM - 12.00 noon) | | | | | IInd Shift (2.00 PM - 4.00 PM) | | | |

Copy to :

1. Secretary to Vice Chancellor for kind information to Hon'ble V C.
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(Prof. R.S.Gupta)
 Coordinator /Professor Incharge
 Faculty of Engineering